FTDI Chip
VI800A-ETH Datasheet
Plug in accessory for VM800P Embedded Video Engine
Plus module

1 Introduction

The VI800A-ETH is a plug in accessory for the VM800P Plus module, which is used to develop and demonstrate the functionality of the FT800 Embedded Video Engine, EVE.

This module behaves as an SPI to Ethernet bridge on the VM800P Plus module.

1.1 Features

- Connects to the VM800P Plus module using an SPI slave interface
- SPI slave interface is converted to an Ethernet interface compliant with IEEE 802.3 10BASE-T and 802.3u 100BASE-TX
- 6 LEDs to indicate Ethernet status. (2 integrated in the RJ45 connector)
- RJ45 connector
- Powered from the VM800P module
2 Ordering Information

<table>
<thead>
<tr>
<th>Part No.</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>VI800A-ETH</td>
<td>VI800A Ethernet module, plug in accessory for the VM800P Plus module</td>
</tr>
</tbody>
</table>

Table 2-1 – Ordering information
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3 Hardware Description

Please refer to section 4.2 for connector settings.

3.1 VI800A-ETH module

The VI800A Ethernet module is designed to connect directly with the VM800P Plus module. The main functions of the VI800A Ethernet module are as follows:

- Plug in accessory board for the VM800P Plus module.
- Interface to the VM800P Plus board as a SPI slave device.
- Connects with an external Ethernet interface.
- Contains 6 LEDs (2 integrated in RJ45 connector).
- Powered by the VM800P Plus board.
4 Physical Descriptions

4.1 Dimensions

The VI800A-ETH module dimension is illustrated in Figure 3-2, Figure 3-3 and Figure 4-34.

Figure 4-1 – VI800A-ETH module Top view

Figure 4-2 – VI800A-ETH module Bottom view
### 4.2 VI800A-ETH Connectors

Connectors and jumpers are described in the following sections.

- **CN1 - RJ45 (LPJ0514GENL)**
  
  This is a standard Ethernet connector with integrated LEDs.

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>TX+</td>
<td>O</td>
<td>Ethernet data TX+ output</td>
</tr>
<tr>
<td>2</td>
<td>TX-</td>
<td>O</td>
<td>Ethernet data TX- output</td>
</tr>
<tr>
<td>3</td>
<td>RX+</td>
<td>I</td>
<td>Ethernet data RX+ input</td>
</tr>
<tr>
<td>4</td>
<td>NC</td>
<td>NC</td>
<td>NOT USED</td>
</tr>
<tr>
<td>5</td>
<td>NC</td>
<td>NC</td>
<td>NOT USED</td>
</tr>
<tr>
<td>6</td>
<td>RX-</td>
<td>I</td>
<td>Ethernet data RX- input</td>
</tr>
<tr>
<td>7</td>
<td>GND</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
<td>GND</td>
<td>Ground</td>
</tr>
</tbody>
</table>

*Table 4-1 – CN1 Pinout*

- **CN2- Plug in Interface**

  This is the interface where the control and data signals from the VM800P boards are routed. There are also power and ground pins on this interface. This interface is used to connect the VI800A Ethernet board to the VM800P Plus board.

  **Note:**

  *This connector should be connected to J6 of the VM800P plus board.*

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SCK</td>
<td>I</td>
<td>SPI Clock input</td>
</tr>
<tr>
<td>2</td>
<td>MOSI</td>
<td>I</td>
<td>SPI Master Out Slave in</td>
</tr>
<tr>
<td>3</td>
<td>MISO</td>
<td>O</td>
<td>SPI Master In Slave out</td>
</tr>
<tr>
<td>4</td>
<td>SS</td>
<td>I</td>
<td>SPI Chip select, active low</td>
</tr>
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</table>

All dimensions are in mm.
### Table 4-2 – CN2 Pinout

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>INT0</td>
<td>O</td>
<td>Interrupt output, active low</td>
</tr>
<tr>
<td>6</td>
<td>IO6</td>
<td>I</td>
<td>Daughter reset input, active low</td>
</tr>
<tr>
<td>7</td>
<td>AD4</td>
<td>IO</td>
<td>Address/Data Line 4</td>
</tr>
<tr>
<td>8</td>
<td>AD5</td>
<td>IO</td>
<td>Address/Data Line 5</td>
</tr>
<tr>
<td>9</td>
<td>3V3</td>
<td>P</td>
<td>3.3V power supply</td>
</tr>
<tr>
<td>10</td>
<td>5V</td>
<td>P</td>
<td>5V power supply</td>
</tr>
<tr>
<td>11</td>
<td>GND</td>
<td>P</td>
<td>Ground</td>
</tr>
<tr>
<td>12</td>
<td>RST#</td>
<td>I</td>
<td>Reset, active low</td>
</tr>
<tr>
<td>13</td>
<td>AD1</td>
<td>IO</td>
<td>Address/Data Line 1</td>
</tr>
<tr>
<td>14</td>
<td>NC</td>
<td>NA</td>
<td>Not Connected</td>
</tr>
<tr>
<td>15</td>
<td>ETH_INT#</td>
<td>O</td>
<td>Interrupt out, active low</td>
</tr>
<tr>
<td>16</td>
<td>AD2</td>
<td>IO</td>
<td>Address/Data Line 2</td>
</tr>
</tbody>
</table>

- **JP1 - POE**
  Jumper connection not fitted by default

### 4.3 VI800A Ethernet Components

- **U1 – W5100**
  This converts the SPI signals from the VM800P Plus board to Ethernet signals. The interface is IEEE 802.3 10BASE-T and 802.3u 100BASE-TX compliant.

- **CN1 – LPJ0514GENL**
  The RJ45 Ethernet connector to connect the Ethernet cable with 2 integrated indicator LEDs.
  The green LED indicates if a link is established or not. If illuminated the link is good.
  The yellow LED indicates the link speed. Illuminated is 100Base-TX and unlit is 10Base-T

- **LED1 – LED4**
  Indicates the status of the Ethernet transmission.

  LED1: This is the FDX LED. It is yellow in colour.
  LED2: This is the collision LED. It is yellow in colour.
  LED3: This is the RX LED. It is green in colour.
  LED4: This is the TX LED. It is green in colour.
5 Board Schematics

Figure 5-1 – VI800A Ethernet Schematics
6 Hardware Setup Guide

6.1 Power Configuration

The board is powered from the VM800P Plus board. The CN2 connector on the VI800A Ethernet board should be connected to the J6 connector of the VM800P Plus board as shown in Figure 5-1.

Figure 6-1 – VI800A Ethernet module connected to VM800P Plus module
7 Contact Information

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Appendix A – References

Document References

VM800P datasheet: VM800P Plus board
FT800 datasheet: FT800_Embedded_Video_Engine
FT800 software programming guide: FT800_Programmer_Guide

FT800 sample application notes:
AN_246_VM800CB_SampleAPP_Arduino_Introduction
AN_275_FT800_Example_with_Arduino.pdf
AN_318_Arduino_Library_for_FT800_Series
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<tr>
<th>Revision</th>
<th>Changes</th>
<th>Date</th>
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<tbody>
<tr>
<td>Version 1.0</td>
<td>Initial Release</td>
<td>2014-10-14</td>
</tr>
<tr>
<td>Version 1.1</td>
<td>Added height dimensions</td>
<td>2014-10-21</td>
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<tr>
<td>Version 1.2</td>
<td>Dual branding to reflect the migration of the product to the Bridgetek name – logo changed, copyright changed, contact information changed</td>
<td>2016-09-15</td>
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